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PATENT
02-CT-495/DP

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Applications Branch
Luca DIFALCO et al.
Serial No.: (Not Yet Assigned)
Filed: HEREWITH
For: PACKAGE FOR SEMICONDUCTOR
DEVICES

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The attached Form PTO-1449 provides a listing of information which may be relevant to the subject application. This IDS is not intended as a representation that better art is not available, nor that other art than that identified exists; nor that the information provided is prior art; nor that a search has been made.

This IDS is submitted under:

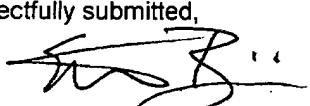
XX 37 CFR 1.97(b) - No Fee.
 37 CFR 1.97(c) - No Fee, with Certification.
 37 CFR 1.97(c) - Fee.
 37 CFR 1.97(d) - Fee and Certification.

The Commissioner is authorized to charge any required fees under 37 CFR 1.17(p) and (i)(1) to Deposit Account No. 50-1556.

Date: 1/27/04

Respectfully submitted,

By:


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Form PTO-1449 U.S. Dept. of Commerce Patent & Trademark Office	Atty. Docket: 02-CT-495/DP	Serial No. Not Yet Assigned
List of Documents Cited by Applicant <small>(Use several sheets if necessary)</small>	Applicant: Luca DIFALCO et al.	
	Filing Date: HEREWITH	

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Sub-class	Transl'n Yes/No
	AA4	2 583 939	December 26, 1986	France			No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	AA5	Schulz-Harder, J. "DBC Substrates as a Base for Power MCM's", Proceedings of 3 rd Electronics Packaging Technology Conference 2000 (EPCT 2000), Singapore, December 2000, pp. 315-320, XP020534488.
	AA6	European Search Report dated August 25, 2003 for European Application No. 03425036.

Examiner:	Date Considered:
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.